

ABSTRACT

Circuits, devices and methods provide a phase delay and use it to select when an
5 analog color signal is converted to digital. The phase delay is adjustable, which permits
choosing a moment in time when conversion results in improved processing. A PLL
circuit receives the synchronizing signal of the color signals, and generates phased
signals. A phase adjuster generates an adjustable delay signal by mixing in suitable
proportions two of the phased signals that are 45 degrees apart. The delay signal is used
10 by an analog to digital converter, to adjust when exactly it is to be sampled.